column address, obtaining information from the memory, and generating internal column

addresses and obtaining further information from the memory until all desired internal column

addresses are used if the read operation of the burst mode of operation is selected; and

selecting an initial buffered external address data path, obtaining an initial external

column address, providing information to the memory, and generating internal column addresses

and providing [proividing] further information to the memory until all desired internal column

addresses are used if the write operation of the burst mode of operation is selected.

83.   (Amended) The method of claim 80, further comprising:

subsequently switching from the pipelined mode of operation to the burst mode of

operation; and

generating an internal column address subsequent to the first external column address for

operation in the burst mode, the internal column address patterned after the first external column

address.

REMARKS

The Appellants' representative has carefully reviewed and considered the Office Action

mailed on March 15, 2002, and the references cited therewith. Claims 63 and 83 are amended to

correct typographical errors, and not for reasons related to patentability. No claims are canceled,

and no claims are added. As a result, claims 36-39, 59-69, and 75-83 remain pending in this

application.

Information Disclosure Statement

The Appellants' representative respectfully requests that a copy of Form 1449, listing all

references that were submitted with the Information Disclosure Statement filed on October 8,

2001, marked as being considered and initialed by the Examiner, be returned with the next

official communication from the Office.